IN THE CLAIMS

Please amend the claims as follows:

1. (Canceled)

2. (Currently Amended) A semiconductor apparatus ~~as claimed in claim 1 further~~ comprising:[[,]]

a processor having an instruction register and an input switchover device;

a pseudorandom number generating device for generating pseudorandom numbers; and

an instruction converting device between the pseudorandom number generating device and input switchover device, ~~the instruction converting device~~ outputting the pseudorandom numbers without change when the pseudorandom numbers inputted from the pseudorandom number generating device are defined instructions, and converting the pseudorandom numbers into the defined instructions when the pseudorandom numbers are undefined instructions to thereby output the defined instructions, wherein:

the input switchover device switches over between data input in normal operation and input of the pseudorandom numbers from the pseudorandom number generating device in the test operation to thereby output the pseudorandom numbers in the test operation to the instruction register, and

the processor operates on the pseudorandom numbers inputted to the instruction register in the test operation.

3. (Canceled)

4. (Currently Amended) A semiconductor apparatus ~~as claimed in claim 3 further~~ comprising:[[,]]

a processor having an instruction register, an input switchover device, and a difficult-to-control circuit part difficult to control by merely setting pseudorandom numbers in the instruction register;

a pseudorandom number generating device generating pseudorandom numbers;

a scan shift controlling device inputting the pseudorandom numbers from the pseudorandom number generating device to the difficult-to-control circuit part in the test operation, and outputting data from the difficult-to-control circuit part; and

an instruction converting device between the pseudorandom number generating device and input switchover device outputting the pseudorandom numbers without change when the pseudorandom numbers inputted from the pseudorandom number generating device are defined instructions, and converting the pseudorandom numbers into the defined instructions when the pseudorandom numbers are undefined instructions to thereby output the defined instructions, wherein:

the input switchover device switches over between data input in normal operation and input of the pseudorandom numbers from the pseudorandom number generating device in the test operation to thereby output the pseudorandom numbers in the test operation to the instruction register, and

the processor operates on the pseudorandom numbers inputted to the instruction register in the test operation.

5 – 7. (Canceled)